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| L<br>Number | Hits | Search Text   | DB  | Time stamp          |
|-------------|------|---|---|---------------------|
| 3           | 0    | 2002/0114224.pn.  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/23<br>09:37 |
| 4           | 2    | 20020114224.pn.   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/23<br>09:40 |
| 5           | 35   | load with data with increas\$3 with delay<br>with transmi\$5  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/23<br>11:19 |
| 6           | 10   | load with data with increas\$3 with delay<br>with transmi\$5 with capacit\$3  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/23<br>11:22 |
| 7           | 1    | data with (increas\$3 adj load) with<br>delay\$1 with transmi\$5 with capacit\$3  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/23<br>11:23 |
| -           | 2    | 20040019844.pn.   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>09:48 |
| -           | 281  | 714/798.ccls.   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>09:48 |
| -           | 1    | (714/798.ccls.) and (clock adj speed) and<br>(data with transmi\$5 with rece\$5 with<br>((integrated adj circuit) or IC))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>09:55 |
| -           | 57   | (clock adj speed) and (data with<br>transmi\$5 with rece\$5 with ((integrated<br>adj circuit) or IC))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>09:56 |
| -           | 2    | (clock adj speed) and (tim\$3 with data<br>with transmi\$5 with rece\$5 with<br>((integrated adj circuit) or IC)) and<br>(clock with generat\$3) and monitor\$3   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:00 |
| -           | 18   | (clock adj speed) and (data with<br>transmi\$5 with rece\$5 with ((integrated<br>adj circuit) or IC)) and (clock with<br>generat\$3) and monitor\$3   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:00 |
| -           | 2    | (clock adj speed) and (data with<br>transmi\$5 with rece\$5 with ((integrated<br>adj circuit) or IC)) and (clock with<br>generat\$3) and monitor\$3 and (timing with<br>data with signal) and (detect\$3 with<br>fail\$3) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:01 |
| -           | 3    | (clock adj speed) and (data with<br>transmi\$5 with rece\$5 with ((integrated<br>adj circuit) or IC)) and (clock with<br>generat\$3) and monitor\$3 and (timing with<br>data with signal)                                 | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:02 |

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|---|----|---|---|---------------------|
| - | 39 | (adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:03 |
| - | 16 | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:04 |
| - | 5  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with transmi\$5 with rece\$5) and fail\$3                | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:06 |
| - | 2  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with transmi\$5 with rece\$5) and fail\$3 and monitor\$3 | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:06 |
| - | 3  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with transmi\$5 with rece\$5) and monitor\$3             | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:10 |
| - | 1  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with transmi\$5 with rece\$5 with timing) and monitor\$3 | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:10 |
| - | 7  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with transmi\$5 with rece\$5)                            | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>11:51 |
| - | 2  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (tim\$3 with data with transmi\$5 with rece\$5)                | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>10:17 |
| - | 3  | ((adjust\$3 or alter\$3) with clock with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with signal with timing) and monitor\$3                  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>11:55 |
| - | 1  | ((adjust\$3 or alter\$3) adj clock) with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with signal with timing) and monitor\$3                  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>11:56 |
| - | 1  | ((adjust\$3 or alter\$3) adj clock) with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with signal with timing)                                 | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>11:56 |
| - | 1  | ((adjust\$3 or alter\$3) adj clock) with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (data with signal with fail\$3)                                | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>11:57 |
| - | 1  | ((adjust\$3 or alter\$3) adj clock) with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3) and (detect\$3 with fail\$3)                                       | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>11:58 |
| - | 5  | ((adjust\$3 or alter\$3) adj clock) with speed with ((integrated adj circuit) or IC)) and (clock adj generat\$3)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>14:54 |

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| - | 2   | (((adjust\$3 or alter\$3) adj clock adj speed) with ((integrated adj circuit) or IC)) and (clock adj generat\$3)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>12:01 |
| - | 3   | (((adjust\$3 or alter\$3) adj clock adj speed) with ((integrated adj circuit) or IC))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>12:02 |
| - | 105 | (((adjust\$3 or alter\$3) adj clock) with ((integrated adj circuit) or IC))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>12:02 |
| - | 18  | (((adjust\$3 or alter\$3) adj clock) with ((integrated adj circuit) or IC))) and ((clock adj generat\$3) with ((integrated adj circuit) or IC)                         | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>12:04 |
| - | 2   | (((integrated adj circuit) or IC) with (clock adj generat\$3)) and (data with transmi\$5 with rece\$5) and (fail\$3 with transmi\$5 with data with detec\$4)           | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>15:02 |
| - | 3   | (((integrated adj circuit) or IC) with (clock adj generat\$3)) and (fail\$3 with transmi\$5 with data with detec\$4)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>15:21 |
| - | 13  | (((integrated adj circuit) or IC) with fail\$3 with transmi\$5 with data with detec\$4)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>15:34 |
| - | 1   | (((integrated adj circuit) or IC) with fail\$3 with transmi\$5 with data with detec\$4) and ECC and (error with check)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:09 |
| - | 1   | (((integrated adj circuit) or IC) with fail\$3 with transmi\$5 with data with detec\$4) and (error with correction with signal with generat\$3) and (error with check) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>15:42 |
| - | 2   | (((integrated adj circuit) or IC) with fail\$3 with transmi\$5 with data with detec\$4) and (error with check)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:07 |
| - | 7   | (((integrated adj circuit) or IC) with (ECC or (error adj correct\$3)) with generat\$3) and (ECC with check)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:16 |
| - | 0   | (((integrated adj circuit) or IC) with (ECC or (error adj correct\$3)) with trasmi\$5) and (ECC with check)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:16 |
| - | 2   | (((integrated adj circuit) or IC) with (ECC or (error adj correct\$3)) with transmi\$5) and (ECC with check)   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:42 |
| - | 19  | clock with speed with ECC  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:46 |

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| - | 3  | clock with speed with ECC with IC   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/21<br>16:47 |
| - | 98 | ("6104331"<br>"6175459"<br>"6535955"<br>"6594794"<br>"6158039"<br>"5740188"<br>"4488222"<br>"5396239"<br>"5838511"<br>"5872790"<br>"5922080"<br>"4456996"<br>"5274646"<br>"5438559"<br>"5500848"<br>"5510903"<br>"5615190"<br>"5701313"<br>"5832001"<br>"5867444"<br>"5987630"<br>"3809884"<br>"4003086"<br>"4360891"<br>"4758992"<br>"4852100"<br>"4868690"<br>"4901228"<br>"5068858"<br>"5220295"<br>"5241546"<br>"5361266"<br>"5369533"<br>"5379415"<br>"5388087"<br>"5390195"<br>"5414722"<br>"5428627"<br>"5442638"<br>"5465261"<br>"5467331"<br>"5469420"<br>"5471351"<br>"5490260"<br>"5495458"<br>"5502573"<br>"5517631"<br>"5537148"<br>"5553231"<br>"5559765") .pn. | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>09:32 |

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| - | 6 | ("6104331"<br>"6175459"<br>"6535955"<br>"6594794"<br>"6158039"<br>"5740188"<br>"4488222"<br>"5396239"<br>"5838511"<br>"5872790"<br>"5922080"<br>"4456996"<br>"5274646"<br>"5438559"<br>"5500848"<br>"5510903"<br>"5615190"<br>"5701313"<br>"5832001"<br>"5867444"<br>"5987630"<br>"3809884"<br>"4003086"<br>"4360891"<br>"4758992"<br>"4852100"<br>"4868690"<br>"4901228"<br>"5068858"<br>"5220295"<br>"5241546"<br>"5361266"<br>"5369533"<br>"5379415"<br>"5388087"<br>"5390195"<br>"5414722"<br>"5428627"<br>"5442638"<br>"5465261"<br>"5467331"<br>"5469420"<br>"5471351"<br>"5490260"<br>"5495458"<br>"5502573"<br>"5517631"<br>"5537148"<br>"5553231"<br>"5559765").pn.) and (clock with speed)<br>and (data with transmi\$5) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>09:26 |
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| - | 2 | ("6104331"<br>"6175459"<br>"6535955"<br>"6594794"<br>"6158039"<br>"5740188"<br>"4488222"<br>"5396239"<br>"5838511"<br>"5872790"<br>"5922080"<br>"4456996"<br>"5274646"<br>"5438559"<br>"5500848"<br>"5510903"<br>"5615190"<br>"5701313"<br>"5832001"<br>"5867444"<br>"5987630"<br>"3809884"<br>"4003086"<br>"4360891"<br>"4758992"<br>"4852100"<br>"4868690"<br>"4901228"<br>"5068858"<br>"5220295"<br>"5241546"<br>"5361266"<br>"5369533"<br>"5379415"<br>"5388087"<br>"5390195"<br>"5414722"<br>"5428627"<br>"5442638"<br>"5465261"<br>"5467331"<br>"5469420"<br>"5471351"<br>"5490260"<br>"5495458"<br>"5502573"<br>"5517631"<br>"5537148"<br>"5553231"<br>"5559765").pn.) and (clock with speed)<br>and (data with transmi\$5) and<br>({integrated adj circuit) or IC) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>09:27 |
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| - | 20 | ("6104331"<br>"6175459"<br>"6535955"<br>"6594794"<br>"6158039"<br>"5740188"<br>"4488222"<br>"5396239"<br>"5838511"<br>"5872790"<br>"5922080"<br>"4456996"<br>"5274646"<br>"5438559"<br>"5500848"<br>"5510903"<br>"5615190"<br>"5701313"<br>"5832001"<br>"5867444"<br>"5987630"<br>"3809884"<br>"4003086"<br>"4360891"<br>"4758992"<br>"4852100"<br>"4868690"<br>"4901228"<br>"5068858"<br>"5220295"<br>"5241546"<br>"5361266"<br>"5369533"<br>"5379415"<br>"5388087"<br>"5390195"<br>"5414722"<br>"5428627"<br>"5442638"<br>"5465261"<br>"5467331"<br>"5469420"<br>"5471351"<br>"5490260"<br>"5495458"<br>"5502573"<br>"5517631"<br>"5537148"<br>"5553231"<br>"5559765").pn.) and clock and<br>((integrated adj circuit) or IC) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>09:34 |
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| - | 19 | ("6104331"<br>"6175459"<br>"6535955"<br>"6594794"<br>"6158039"<br>"5740188"<br>"4488222"<br>"5396239"<br>"5838511"<br>"5872790"<br>"5922080"<br>"4456996"<br>"5274646"<br>"5438559"<br>"5500848"<br>"5510903"<br>"5615190"<br>"5701313"<br>"5832001"<br>"5867444"<br>"5987630"<br>"3809884"<br>"4003086"<br>"4360891"<br>"4758992"<br>"4852100"<br>"4868690"<br>"4901228"<br>"5068858"<br>"5220295"<br>"5241546"<br>"5361266"<br>"5369533"<br>"5379415"<br>"5388087"<br>"5390195"<br>"5414722"<br>"5428627"<br>"5442638"<br>"5465261"<br>"5467331"<br>"5469420"<br>"5471351"<br>"5490260"<br>"5495458"<br>"5502573"<br>"5517631"<br>"5537148"<br>"5553231"<br>"5559765").pn.) and clock and<br>((integrated adj circuit) or IC) and<br>(error adj correct\$3) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>10:46 |
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| - | 0    | ("6104331"<br>"6175459"<br>"6535955"<br>"6594794"<br>"6158039"<br>"5740188"<br>"4488222"<br>"5396239"<br>"5838511"<br>"5872790"<br>"5922080"<br>"4456996"<br>"5274646"<br>"5438559"<br>"5500848"<br>"5510903"<br>"5615190"<br>"5701313"<br>"5832001"<br>"5867444"<br>"5987630"<br>"3809884"<br>"4003086"<br>"4360891"<br>"4758992"<br>"4852100"<br>"4868690"<br>"4901228"<br>"5068858"<br>"5220295"<br>"5241546"<br>"5361266"<br>"5369533"<br>"5379415"<br>"5388087"<br>"5390195"<br>"5414722"<br>"5428627"<br>"5442638"<br>"5465261"<br>"5467331"<br>"5469420"<br>"5471351"<br>"5490260"<br>"5495458"<br>"5502573"<br>"5517631"<br>"5537148"<br>"5553231"<br>"5559765").pn.) and (data adj transmi\$5)<br>with (IC or (integrated adj circuit)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>10:47 |
| - | 2847 | (data adj transmi\$5) with (IC or (integrated adj circuit))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>10:48 |
| - | 1    | (data adj transmi\$5) with (IC or (integrated adj circuit)) with timing with monitor\$3  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>11:42 |
| - | 16   | (data adj transmi\$5) with (IC or (integrated adj circuit)) with timing with clock   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>10:58 |
| - | 3    | ((data adj transmi\$5) with (IC or (integrated adj circuit)) with timing with clock) and (data with transmi\$5 with (fail\$3 or error))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>11:06 |

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| - | 6   | ((data adj transmi\$5) with (IC or (integrated adj circuit)) with timing ) and (data with IC with transmi\$5 with (fail\$3 or error)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>11:06 |
| - | 76  | (data adj transmi\$5) with (IC or (integrated adj circuit)) with timing   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>11:50 |
| - | 195 | (clock adj speed) with data with transmi\$5   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>11:43 |
| - | 16  | (data adj transmi\$5) with (IC or (integrated adj circuit)) with timing with clock  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>12:34 |
| - | 2   | 6509788.pn.   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:00 |
| - | 0   | (clock adj generator) with multiplexor with taps with (adjust\$3 or alter\$3) with speed  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:01 |
| - | 1   | (clock adj generator) with multiplexor with taps with (adjust\$5 or alter\$5) with speed  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:04 |
| - | 1   | (clock adj generator) with multiplexor with taps with speed   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:03 |
| - | 1   | (clock adj generator) with multiplexor with taps  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:04 |
| - | 1   | (clock adj generator) with multiplexor with (adjust\$5 or alter\$5 or chang\$3) with speed  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:05 |
| - | 6   | (clock adj generator) with multiplexor with speed   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:08 |
| - | 91  | (clock adj generator) with multiplexor  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:08 |
| - | 0   | (clock adj generator) adj comprises adj multiplexor   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:08 |
| - | 0   | (clock adj generator) adj comprises adj multiplex\$3  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:09 |

|   |    |   |   |                     |
|---|----|---|---|---------------------|
| - | 91 | (clock adj generator) with multiplexor  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:14 |
| - | 3  | (clock adj generator) adj multiplexor   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>14:59 |
| - | 2  | 6748567.pn.   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>15:54 |
| - | 1  | (random adj data) with IC with transmi\$5<br>with (error\$1 or fail\$3 or test\$3 or<br>verify\$3)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>17:39 |
| - | 1  | (random adj number adj generator) with<br>seed with generat\$3 with (unique adj<br>random adj data) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/22<br>17:41 |
| - | 8  | (random adj number adj generator) with<br>seed with generat\$3 with (random adj<br>data)            | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | 2004/09/23<br>09:37 |